



In Place of FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANTS' INFORMATION DISCLOSURE
STATEMENT**

Serial No.: 09/551,790
Applicants: W. G. Holland, et al.
Filing Date: August 18, 2000
Group: 2188
Atty. Docket No.: RAL920000057US1

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner
Initial

bgA

VLIW Hardware Stack Processor, Kiyoshige Nakamura et al. Information Processing Society of Japan, Search Report, Vol.95, No.80 (95-ARC-113) P.9-P16 / Publication date : 08/25/95

bgA

Programmable Engine Architecture for High-speed Network Processing, Hideyuki Shimonishi et al. The Institute of Electronics, Information and Communication Engineers, Technical Report of Ieice SSE99-127 PS99-51 (1999-12) ISSN: 0913-5685 Vol. 99 No. 507 P.31-P. 36 / Publication date: 12/17/99

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Not
considered
in Japanese